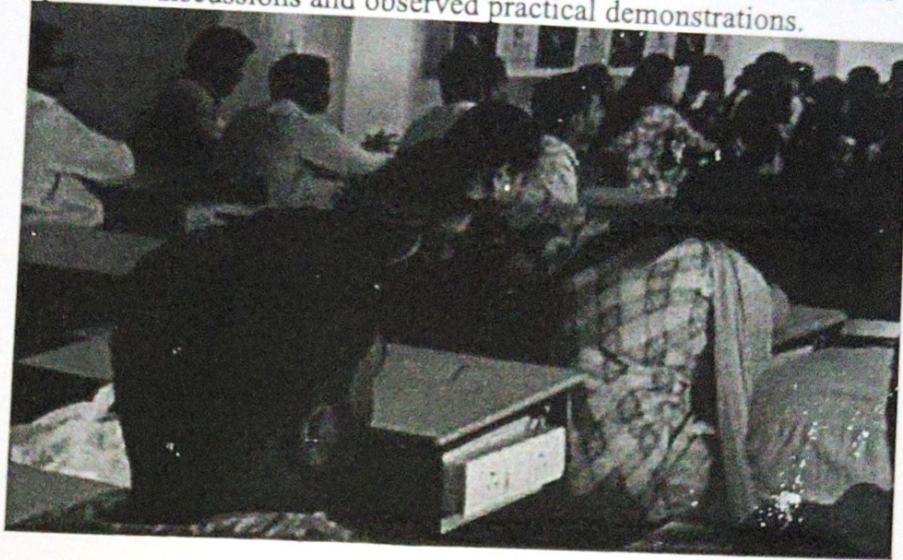
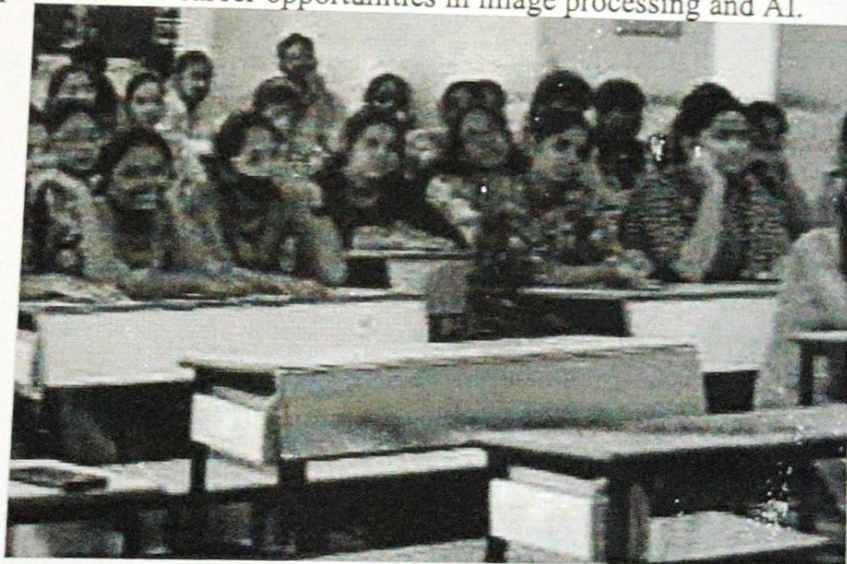


Day-wise Report

Day 1: The first day focused on the fundamentals of digital image processing, including image acquisition, representation, enhancement, histogram processing, filtering techniques, and noise removal methods. The resource person explained the importance of image quality improvement and demonstrated several image processing operations using appropriate software tools. Students actively participated in discussions and observed practical demonstrations.

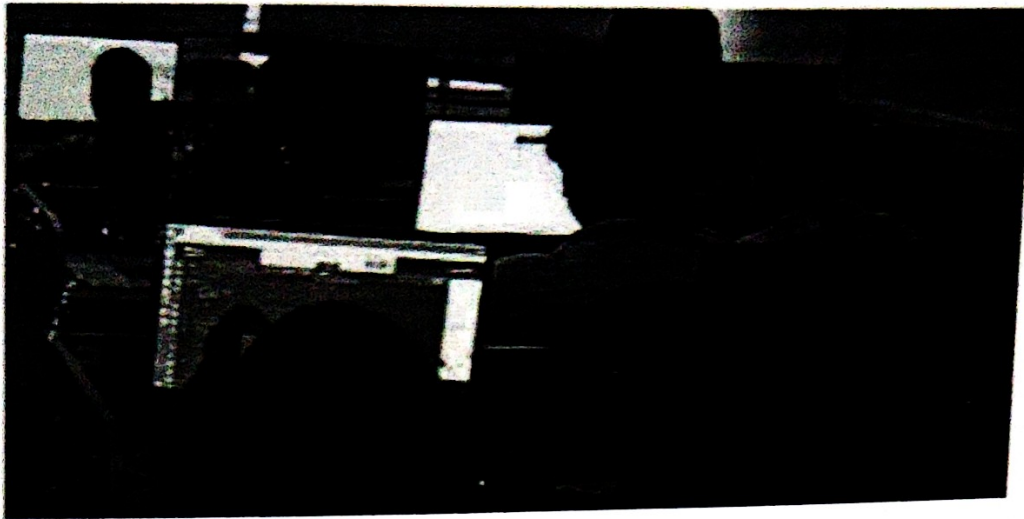


Day 2: The second day covered advanced topics such as image segmentation, feature extraction, pattern recognition, object detection, face recognition, and computer vision applications. Real-world applications in healthcare, surveillance, automation, and artificial intelligence were discussed. The workshop concluded with an interactive session where students clarified their doubts and explored future career opportunities in image processing and AI.



Program Highlights

The workshop featured expert lectures delivered by an industry professional with extensive experience in image processing and artificial intelligence. Students were exposed to practical demonstrations of image enhancement and computer vision algorithms, along with industrial case studies highlighting real-world applications. Interactive sessions, software demonstrations, and discussions on current research trends enhanced students' understanding of the subject and motivated them to pursue projects in this emerging field.



PO-PSO-SDG Mapping

Program Outcomes (POs)	Program Specific Outcomes (PSOs)	Sustainable Development Goals (SDGs)
PO1 – Engineering Knowledge	PSO1 – Apply Digital System Design Principles	SDG 4 – Quality Education
PO2 – Problem Analysis	PSO2 – Design Reliable Embedded and Digital Systems	SDG 8 – Decent Work & Economic Growth
PO3 – Design & Development of Solutions		SDG 9 – Industry, Innovation & Infrastructure
PO5 – Modern Tool Usage		
PO9 – Individual & Team Work		
PO12 – Life-long Learning		

Outcome Analysis

Assessment Parameter	Analysis
Student Participation	Excellent participation during technical sessions
Knowledge Improvement	Significant enhancement in digital design and reliability concepts
Practical Understanding	Improved through case studies and demonstrations
Student Feedback	Highly positive
Overall Effectiveness	Workshop objectives were successfully achieved

Overall Impact

The workshop strengthened students' understanding of digital system design and fault-tolerant architectures by integrating theoretical concepts with industrial practices. The sessions enhanced students' analytical abilities, improved awareness of hardware reliability techniques, and prepared them for careers in VLSI, embedded systems, and digital hardware design.

Prasanth
 HoD
 Head of the Department
 Electronics & Communication Engineering
 Narasimha Reddy Education Society
 Group of Institutions - Integrated Campus
 (M. Nagariguda/VIII), Ghatkesar (Mdl), R.R. Dist. 500 0